Amendments to the Specification:

Please replace paragraph [0003] with the following amended paragraph:

[0003]   This application is a continuation of U.S. Patent Application Serial No. 10/201,831, filed July 23, 2002, <u>which issued on March 30, 2004 as U. S. Patent No. 6,714,614,</u> which is a continuation of U.S. Patent Application Serial No. 09/685,156, filed October 10, 2000, which issued on August 6, 2002 as U.S. Patent No. 6,430,246, which is a continuation of U.S. Patent Application Serial No. 09/420,710, filed October 19, 1999, which issued on November 14, 2000 as U.S. Patent No. 6,148,053, which is a continuation of U.S. Patent Application Serial No. 08/949,027, filed October 10, 1997, which issued on December 28, 1999 as U.S. Patent No. 6,009,135, which are incorporated by reference as if fully set forth.

Please replace paragraph [0013] with the following amended paragraph:

[0013]   A circuit or software generates a cipher stream. The software models components or the circuit comprises a first and a second plurality of linear feedback shift registers (LFSR). A first of the second plurality of LFSR has a clock signal as a clock input and others of the ~~first~~ <u>second</u> plurality of LFSR <u>each</u> have an output of ~~another~~ <u>a previous one</u> of the ~~first~~ <u>second</u> plurality of LFSR as a clock input. A first of the first plurality of LFSR has the clock signal combined with an output of the

first of the second plurality of LFSR as a clock input and others of the ~~second~~ first

plurality of LFSR each have an output of a previous one of the ~~first~~ second plurality

of LSFR combined with an output of ~~another~~ a previous one of the first plurality of

LFSR as a clock input. An output of a last of the first plurality of LFSR and an

output of a last of the second plurality of LFSR is combined to produce the cipher

stream.

Please replace the Abstract with the following new Abstract: